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hereby re 37 CFR 3.7	voke all pre 73(b).	evious powers of attorney	given In the	applica	tion identified in	the attach	ed state	ment under
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any and all o	atent applicati	o represent the undersigned before assigned only to the understordance with 37 CFR 3.73(b).	ore the United igned accordin	otates Pa g to the U	SPTO assignment	records or ass	signment	documents
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A copy of	this form, t	ogether with a statement u	nder 37 CFR	3.73(b)	(Form PTO/SB/S	86 or equiva	ient) is I	equired to be
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and must	noners appoint	application in which this P	ower of Atto	mey is	to be filed.			
			ATURE of Ass	ionee of	Record	behalf of the	assignee	
Signature		Mad B. R	enes	_	1		FEB	2005
Name	Michae	1 E. Marion				Telephone (		333-963
Title		ized Representa	ative					
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This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiallty is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

10/538369 JC20 Rec'd PCT/PTO 1 3 JUN 2005

## **STATEMENT UNDER 37 CFR 3.73(b)**

Applicant/Patent Owner: Koninklijke Philips Electronics N.C.								
Application No./Patent No.: Concurrently Filed/Issue Date: Concurrently								
Entitled: Modular integration of an array processor within a system on chip								
Koninklijke Philips Electronics N.V., a corporation states that it is:								
the assignee of the entire right, title and interest,								
an assignee of less than the entire right, title and interest.  The extent (by percentage) of its ownership interest is% in the patent application/patent identified above,								
by virtue of:								
An assignment from the in The assignment was reco	orded in the United S	States Patent and Tra	nt identified above. ademark Office at					
A chain of title from the in the current assignee as sl	ventor(s), of the pate hown below:	ent application/paten	t identified above, to					
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☐ Additional documents i	in the chain of title a	re listed on a supple	mental sheet.					
Copies of assignments or other documents in the chain of title are attached. [Note: A separate copy ( <i>i.e.</i> , the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]								
The undersigned (whose title	The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.							
Date: 6/10/05	R	espectfully submitted	d,					
	D)	Michael J. Ure, Re						

Title: Patent Attorney
Tel: (408) 474-9077

PTO/SB/01 (03-01)

Approved for use through 10/31/2002. OMB 0651-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION			Attorney Docket Numbe	r US020543
			First Named Inventor	Geoffrey F. Burns, et.al.
			COMPLETE IF KNOWN	
(37 CFR 1.63)		Application Number	1	
☑Declaration Submitted With Initial	OR	☐Declaration Submitted after Initial	Filing Date	
	Filing (surcharge (37 CFR 1.16 (e)) required)	• • •	Group Art Unit	
Filing		Examiner Name		
			-	

As a below named inventor, I hereby declare that:								
My residence, post office address, and citizenship are as stated below next to my name.								
I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names								
are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:  MODULAR INTEGRATION OF AN ARRAY PROCESSOR WITHIN A SYSTEM ON CHIP								
the specification of which	(Title of th	e Invention)						
is attached hereto								
OR								
was filed on (MM/DD	MYYY)	as United States App	olication Number o	PCT International				
Application Number	and	was amended on (MM/DD/Y)	M)	(if	applicable).			
I hereby state that I have revie specifically referred to above.	wed and understand the conte	nts of the above identified spe	ecification, including	g the claims as am	ended			
I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.								
I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or of any PCT international application having a filing date before that of the application on which priority is claimed.								
Prior Foreign Application		Foreign Filing Date	Priority	Certified Copy	Attached?			
Number(s)	Country	(MM/DD/YYYY) Country	Not Claimed	YES	NO			
Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:								

[Page 1 of 2]
Burden Hour Statement: This form is estimated to take 21 minutes to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450.

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## **DECLARATION** — Utility or Design Patent Application

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Name: PHILIPS INTELLECTUAL F	PROPERTY &					
Address: P. O. Box 3001						
City: Briarcliff Manor		State NY				ZIP 10510-8001
Country U.S.A.		Telephone: (	914) 3	332-0222		Fov. (044 000 004
I hereby declare that all statements made believed to be true; and further that these punishable by fine or imprisonment, or bo application or any patent issued thereon.	herein of my own statements were th, under 18 U.S.	n knowledge are t	true an	d that all st	atements ul false sta tatements	Fax: (914 332-0615  made on information and belief are atements and the like so made are a may jeopardize the validity of the
NAME OF SOLE OR FIRST INV	ENTOR:	☐ A petition	on ha	s been fi	led for t	his unsigned inventor
Given Name Geoff (first and middle [if any])	rey E		Far	nily Name		
Inventor's Signature & Australy	E P		<b>-</b>		Date ×	12/4/2003
Residence: City		СТ		U.S.	Α.	USA
11 Peacable Road		State		Countr	у	Citizenship
Mailing Address						
Ridgefield						
City		CT State		06877		USA
NAME OF SECOND INVENTOR:	□ A ne		n Ela	Zip		Country
Given Name  Krishna  first and middle [if any])		etition has bee	Fami	ily Name		ed inventor yanathan
nventor's iignature			or st	urname		
Bannerghatta-Road^M \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		Bangalore		India		INI
esidence: City		State		Country		IN Citizenship
J-301 Mantri Paradise V						
ailing Address						
Bannergjatta Road^M		Bangalore	T	560076		IN
ty		State		Zip		Country
Additional inventors are being name	d on the	supplemental Ado	litional	Inventor(s	) sheet(s)	PTO/SB/02A attached hereto

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DECLARATION FOR UTILITY OR DESIGN PATENT APPLICATION			Attorney Docket Number	US020543
			First Named Inventor	Geoffrey F. Burns, et.al.
			COMPLETE IF KNOWN	
(37 CFR 1.63)		Application Number	1	
☑Declaration Submitted	Declaration Submitted after Initial Filing (surcharge (37 CFR 1.16 (e)) required)	Submitted after Initial Filing (surcharge	Filing Date	
With Initial			Group Art Unit	
Filing		Examiner Name		

As a below named inve	As a below named inventor, I hereby declare that:								
My residence, post office address, and citizenship are as stated below next to my name.									
I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names									
are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:  MODULAR INTEGRATION OF AN ARRAY PROCESSOR WITHIN A SYSTEM ON CHIP									
the specification of which	(Title of th	e Invention)							
☑ is attached hereto									
OR	2000								
□ was filed on (MM/DD/YYYY) as United States Application Number or PCT International									
Application Number and was amended on (MM/DD/YYYY) (if applicable).									
I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended specifically referred to above.									
I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.									
I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or of any PCT international application having a filing date before that of the application on which priority is claimed.									
Prior Foreign Application	Country	Foreign Filing Date	Priority Not Claimed	Certified Co	opy Attached?				
Number(s)	Country	(MM/DD/YYYY) Country	Not Claimed	YES	NO				
Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:									

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Country

supplemental Additional Inventor(s) sheet(s) PTO/SB/02A attached hereto.

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## Utility or Design Patent Application Direct all correspondence to: \*24737\* X Customer Number or Bar Code Label OR Correspondence address below PATENT TRADEMARK OFFICE Name: PHILIPS INTELLECTUAL PROPERTY & STANDARDS Address: P. O. Box 3001 City: Briarcliff Manor State NY ZIP 10510-8001 Country U.S.A. Telephone: (914) 332-0222 Fax: (914 332-0615 I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that wilful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such wilful false statements may jeopardize the validity of the NAME OF SOLE OR FIRST INVENTOR: A petition has been filed for this unsigned inventor Given Name Geoffrey F. **Family Name** Burns (first and middle [if any]) or Surname Inventor's Signature Date Ridgefield CT U.S.A. USA Residence: City State Country Citizenship 11 Peacable Road **Mailing Address** Ridgefield CT 06877 USA City State Zip Country NAME OF SECOND INVENTOR: A petition has been filed for this unsigned inventor Given Name Krishnam urthy **Family Name** (first and middle [if any] Vaidyanathan or Surname inventor's Signature 12/03 Bannerghatta Road^M Bangalore India IN Residence: City State Country Citizenship J-301 Mantri Paradise Mailing Address Bannergjatta Road^M Bangalore 560076 IN City State Zip

Additional inventors are being named on the